

FIG. 1A



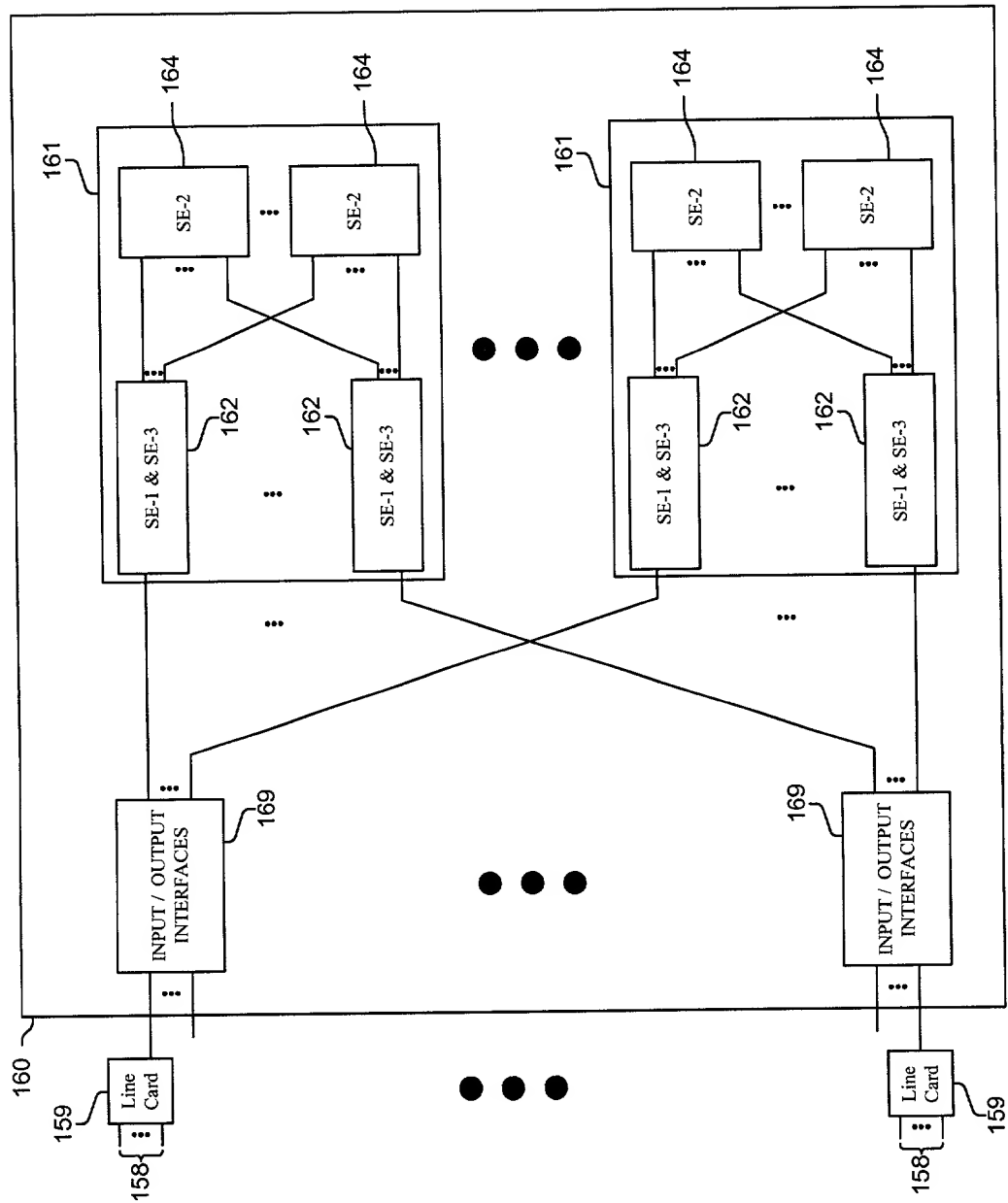


FIG. 1C

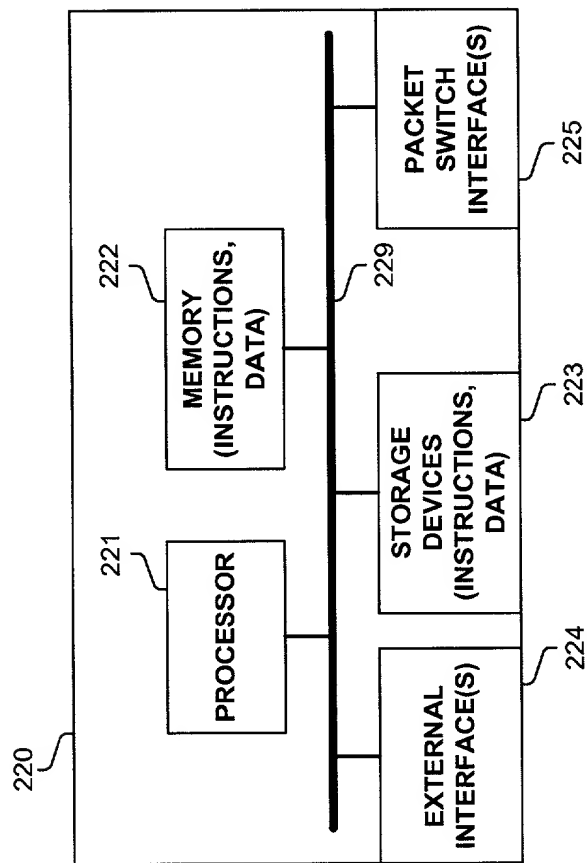


FIG. 2A

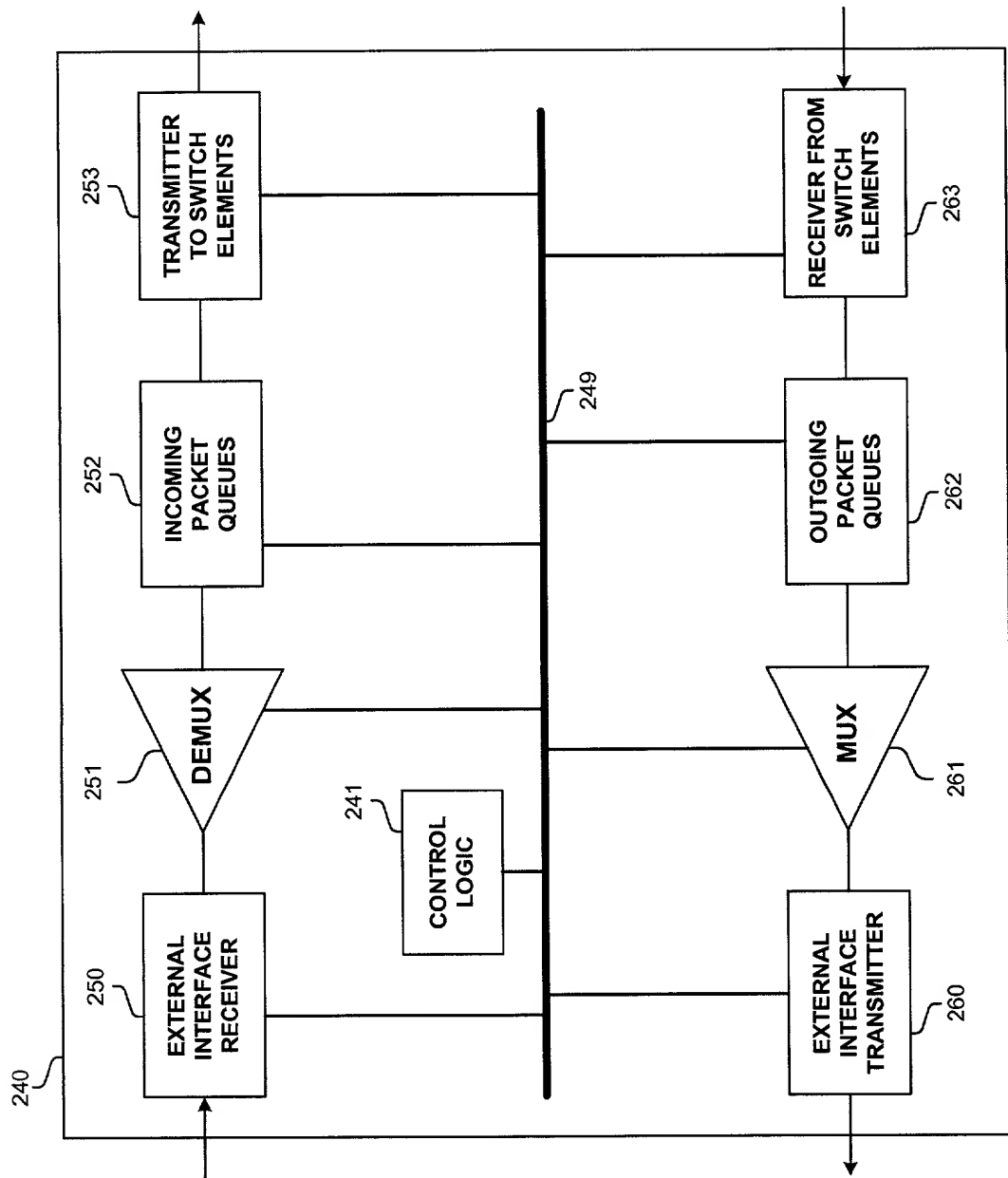


FIG. 2B

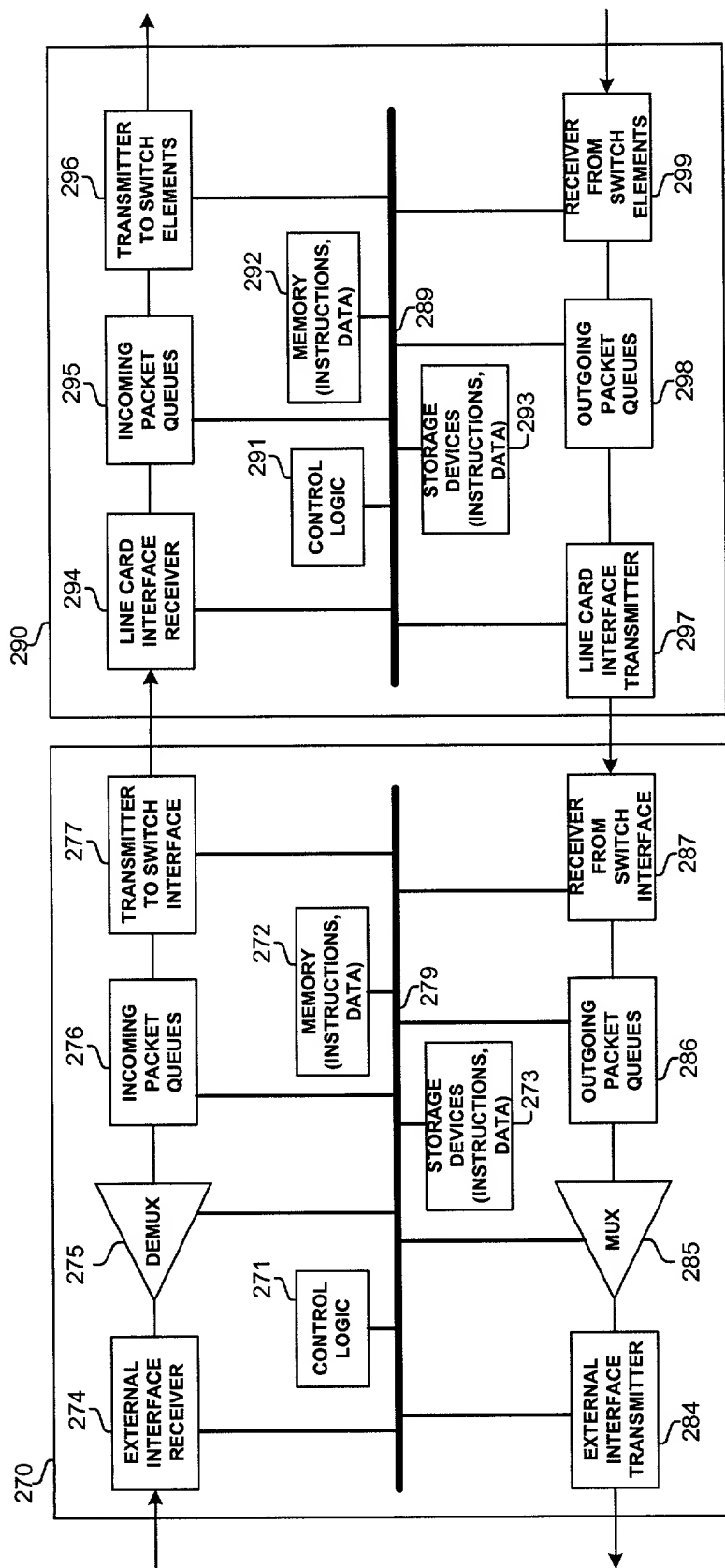


FIG. 2C

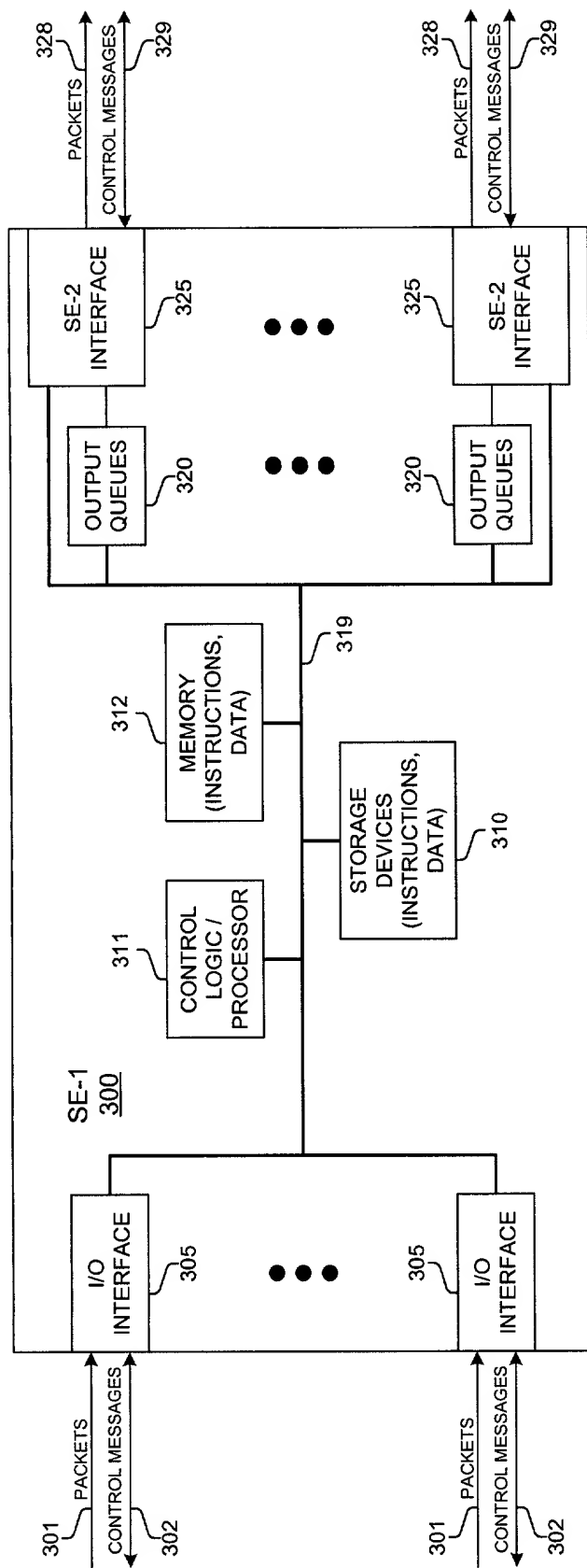


FIG. 3A

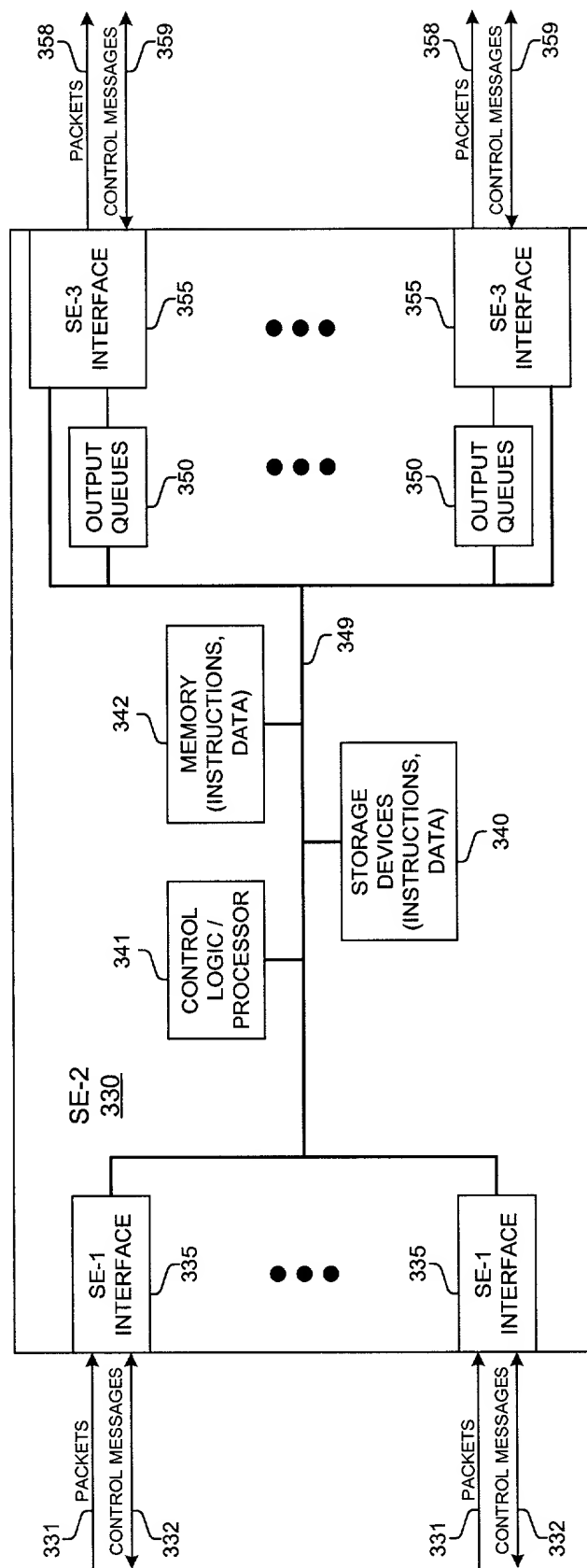


FIG. 3B



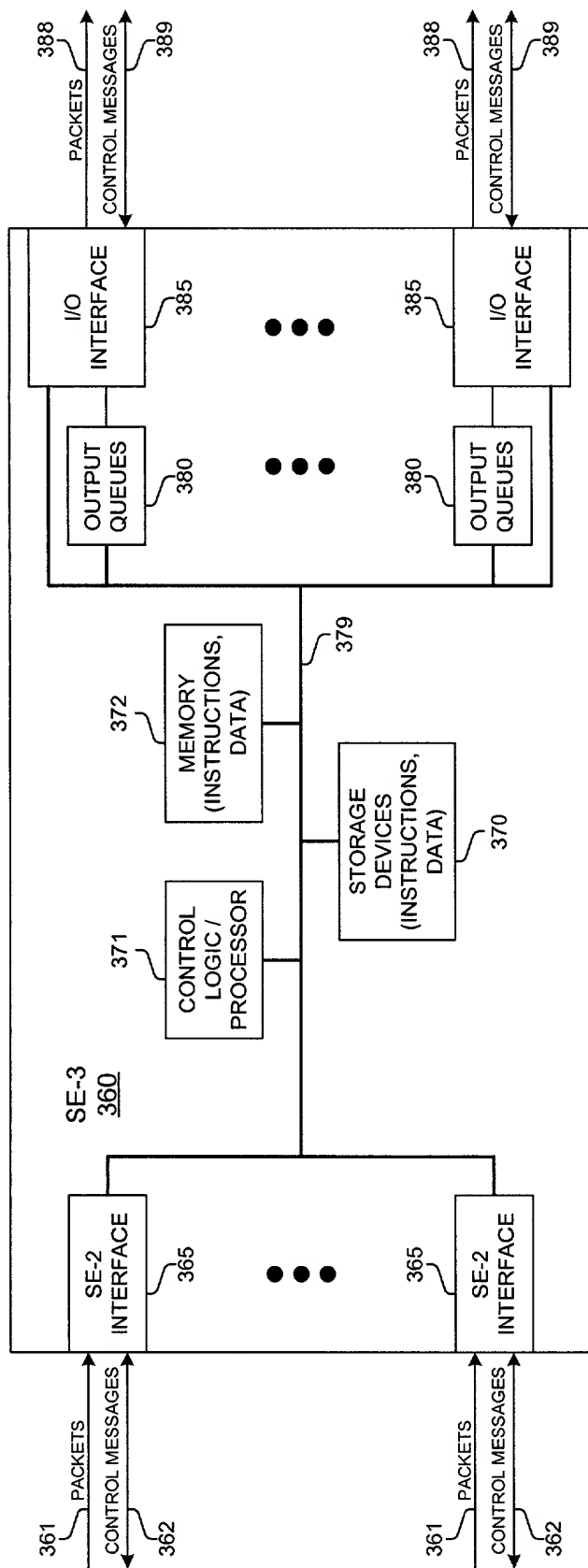


FIG. 30

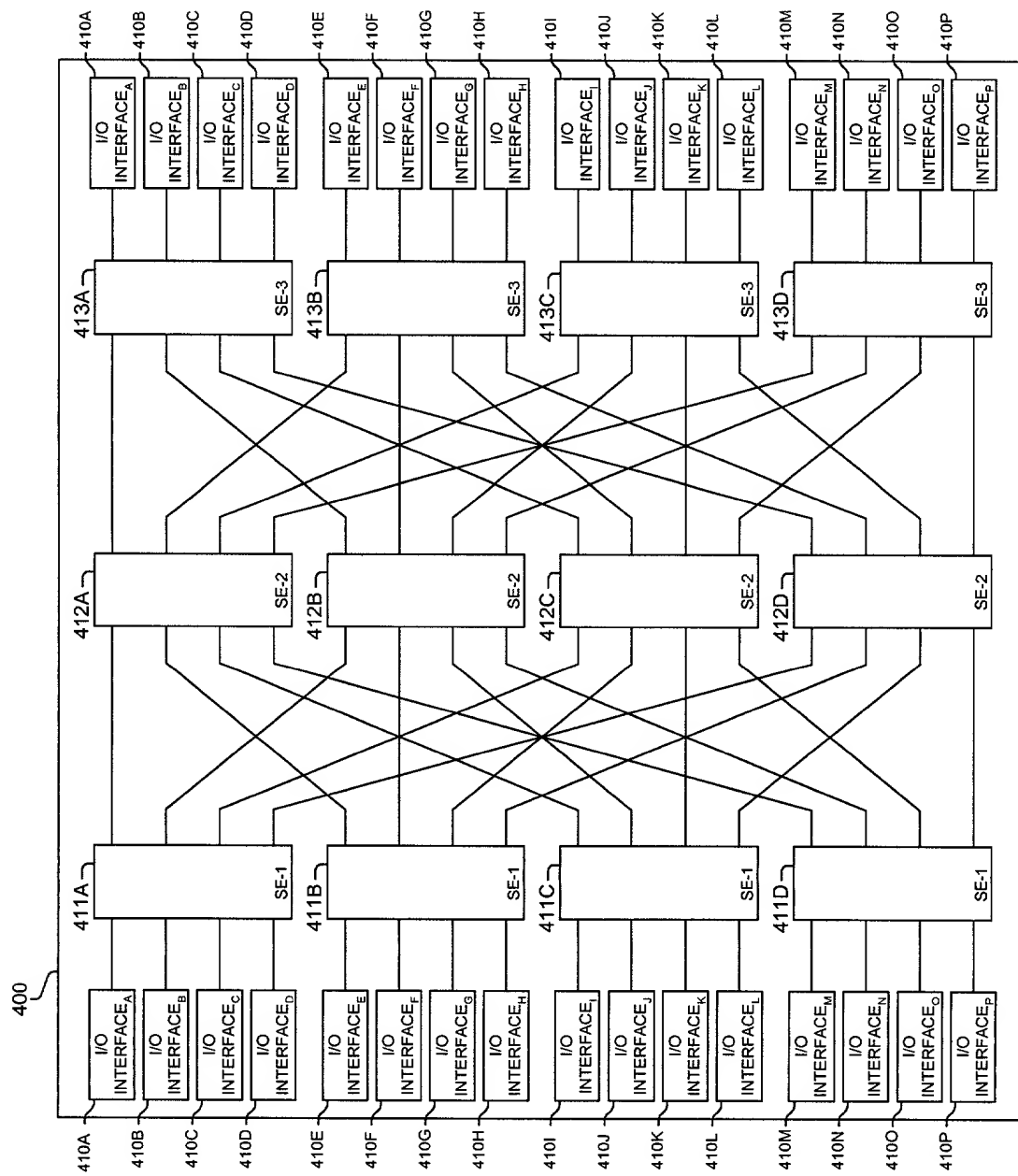


FIGURE 4A

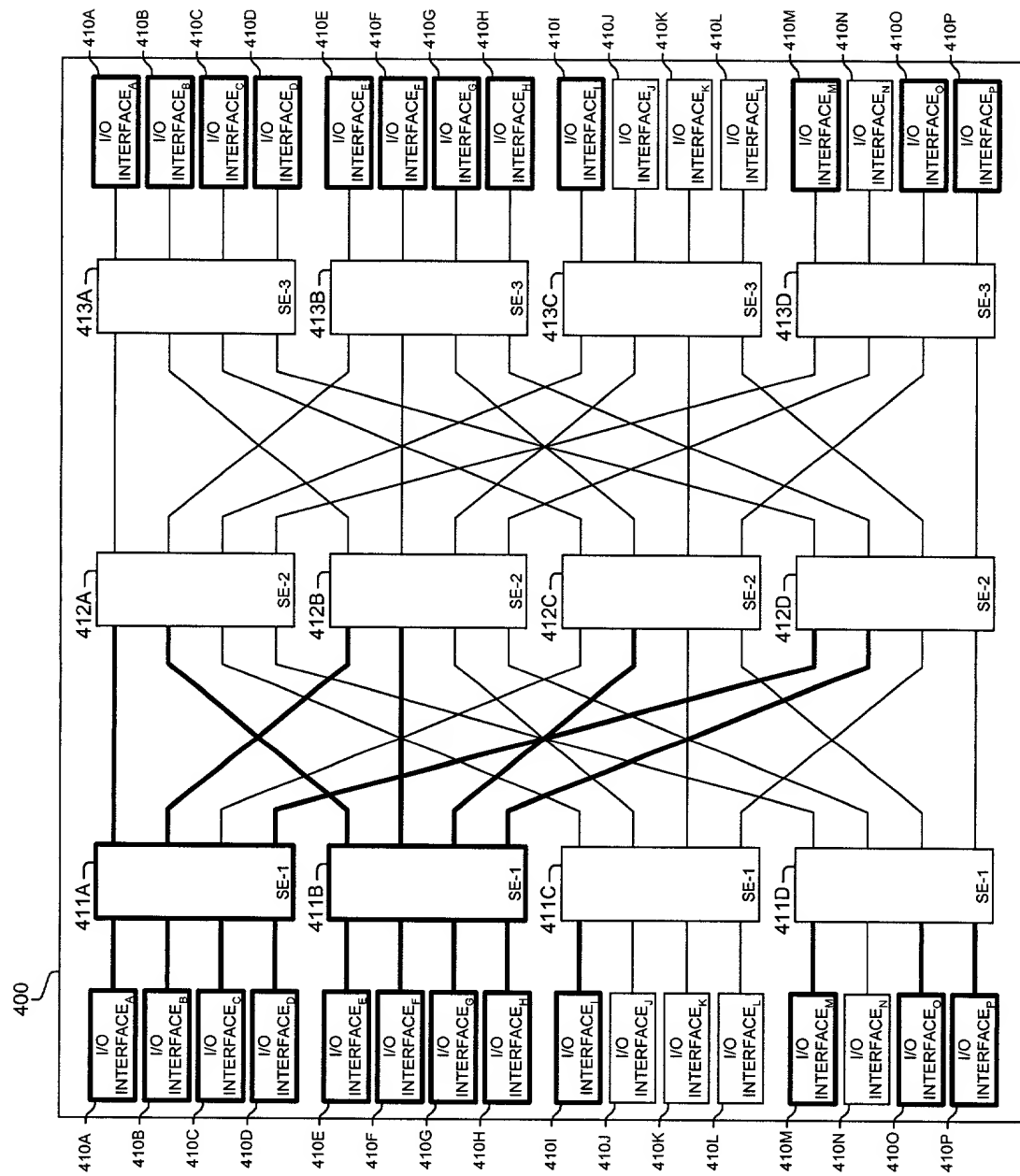


FIGURE 4B

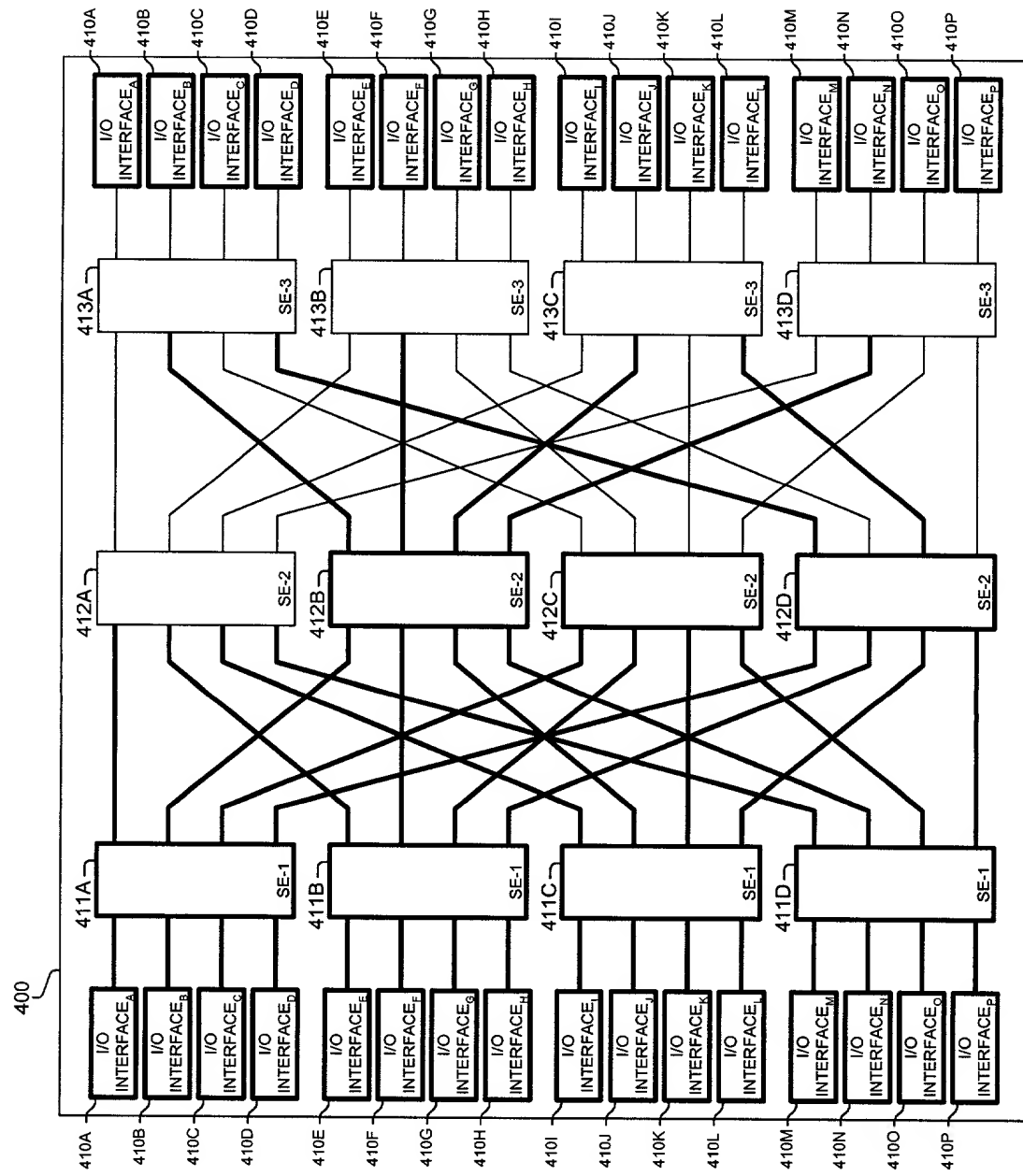


FIGURE 4C

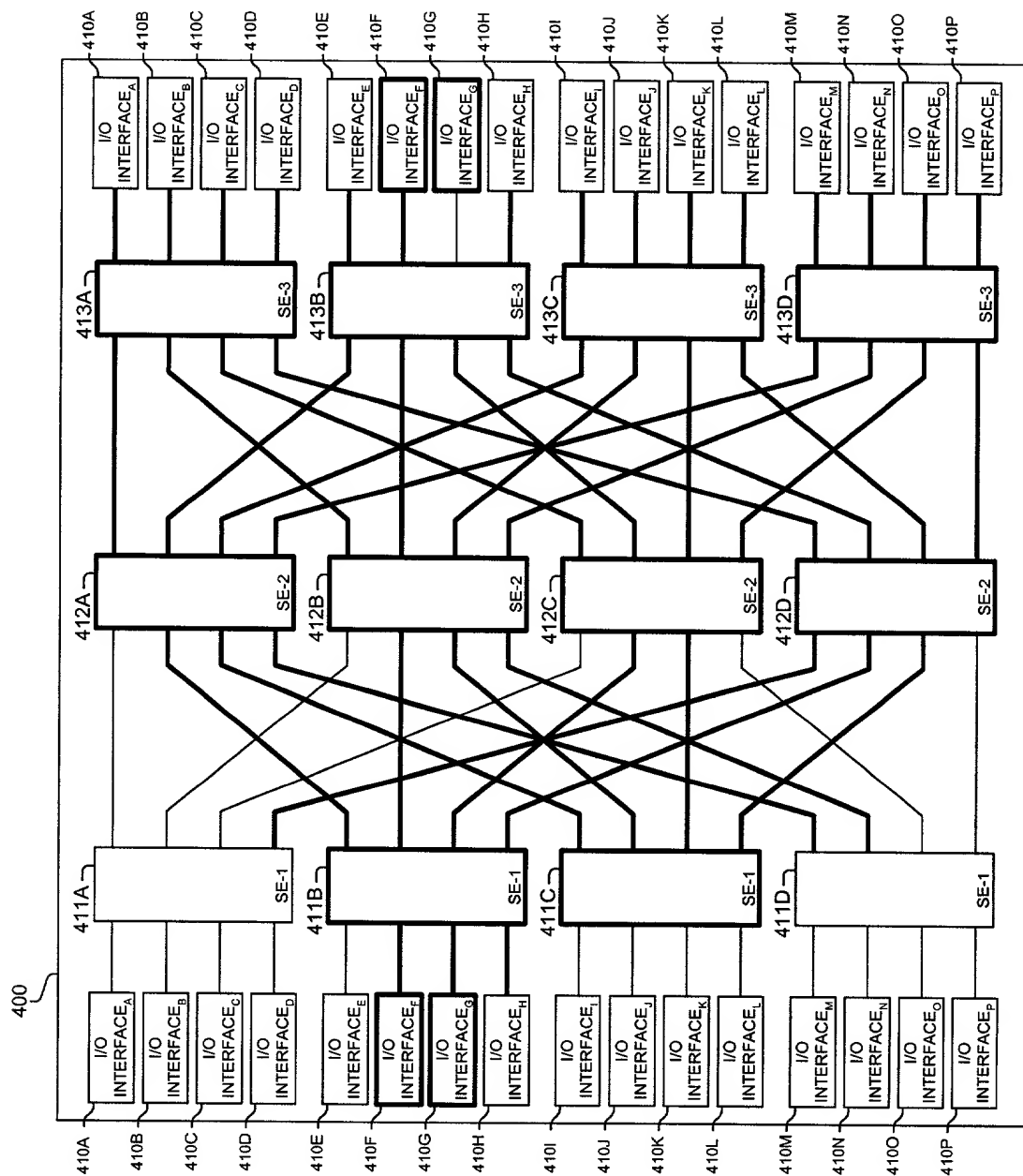


FIGURE 4D

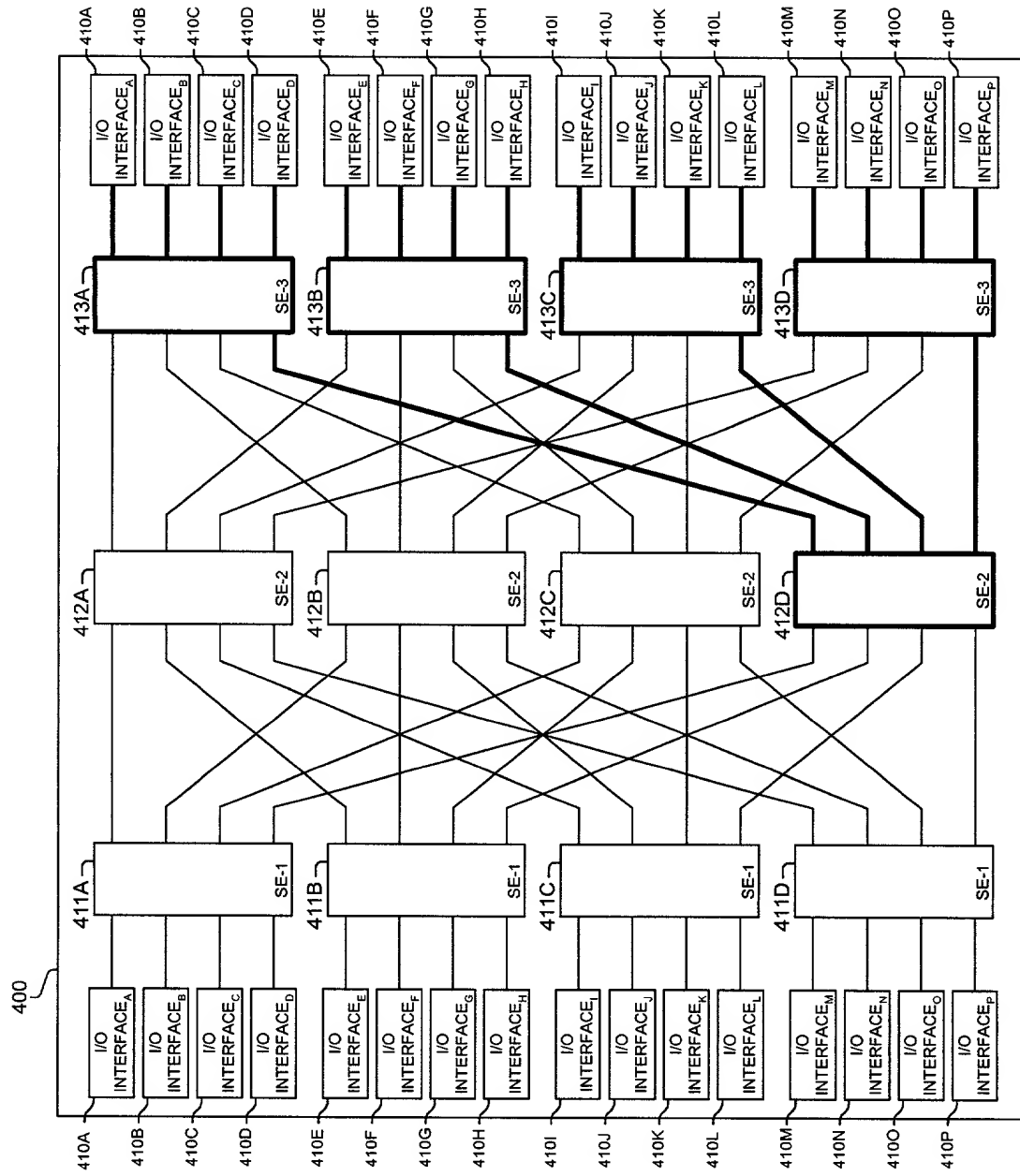


FIGURE 4E

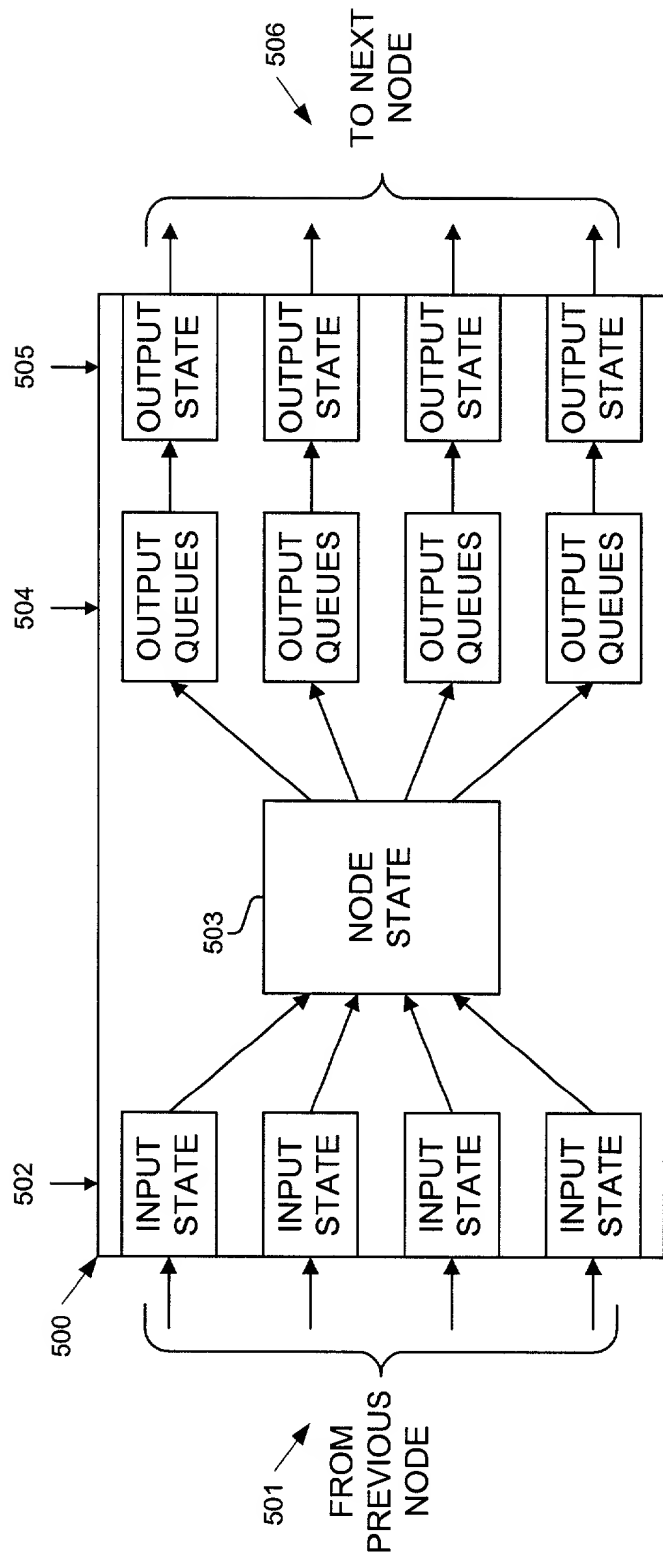


FIGURE 5A

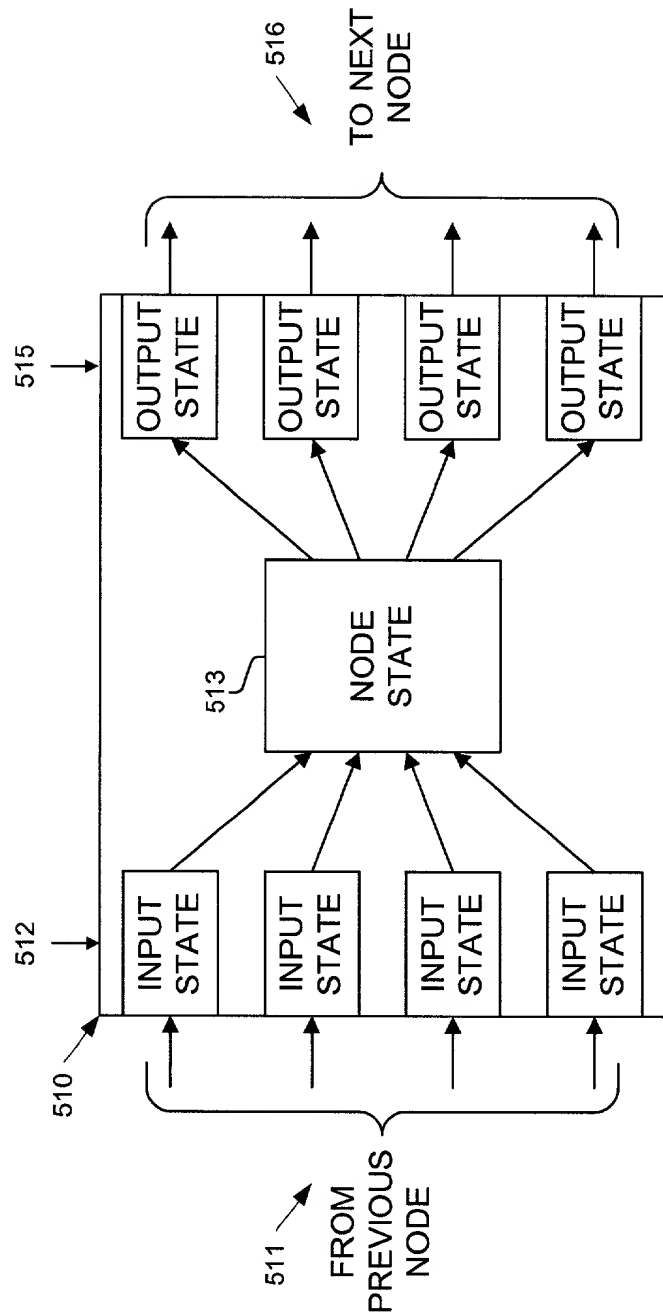
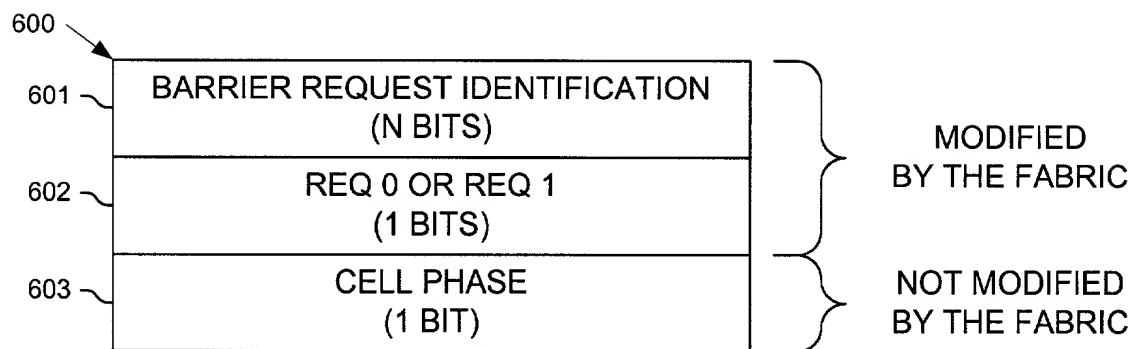


FIGURE 5B





**FIGURE 6A**

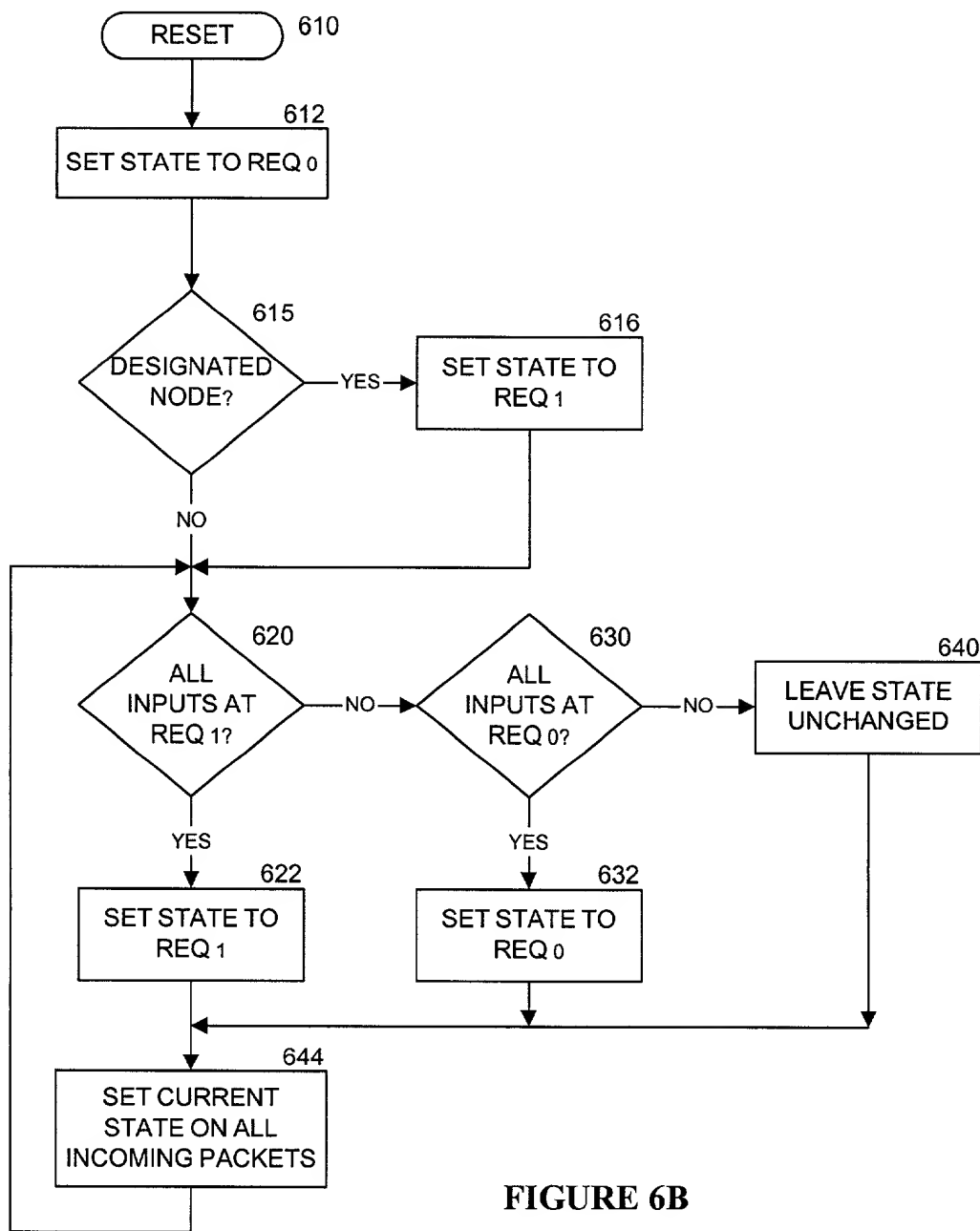
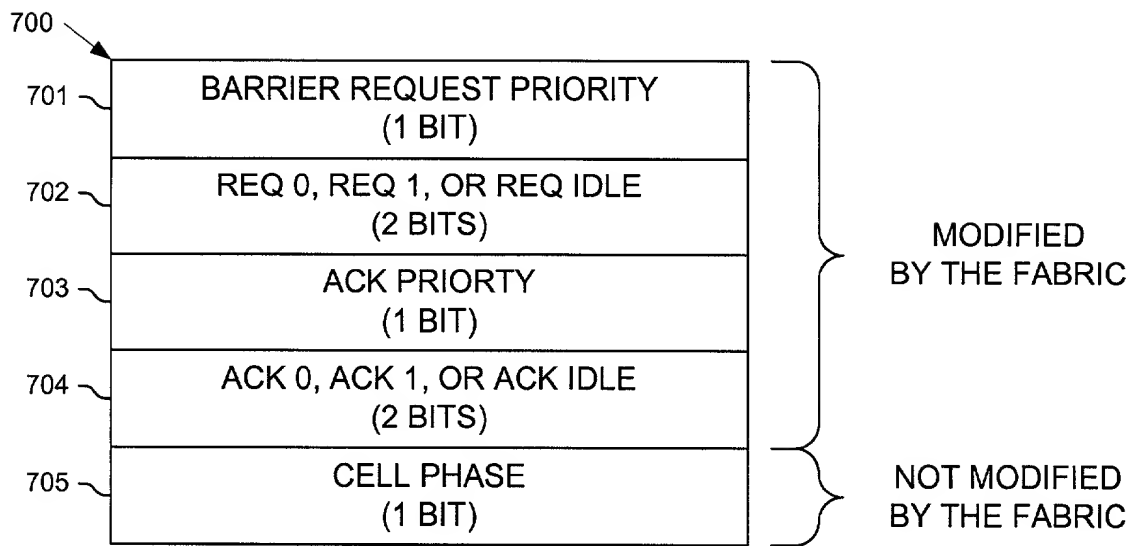


FIGURE 6B



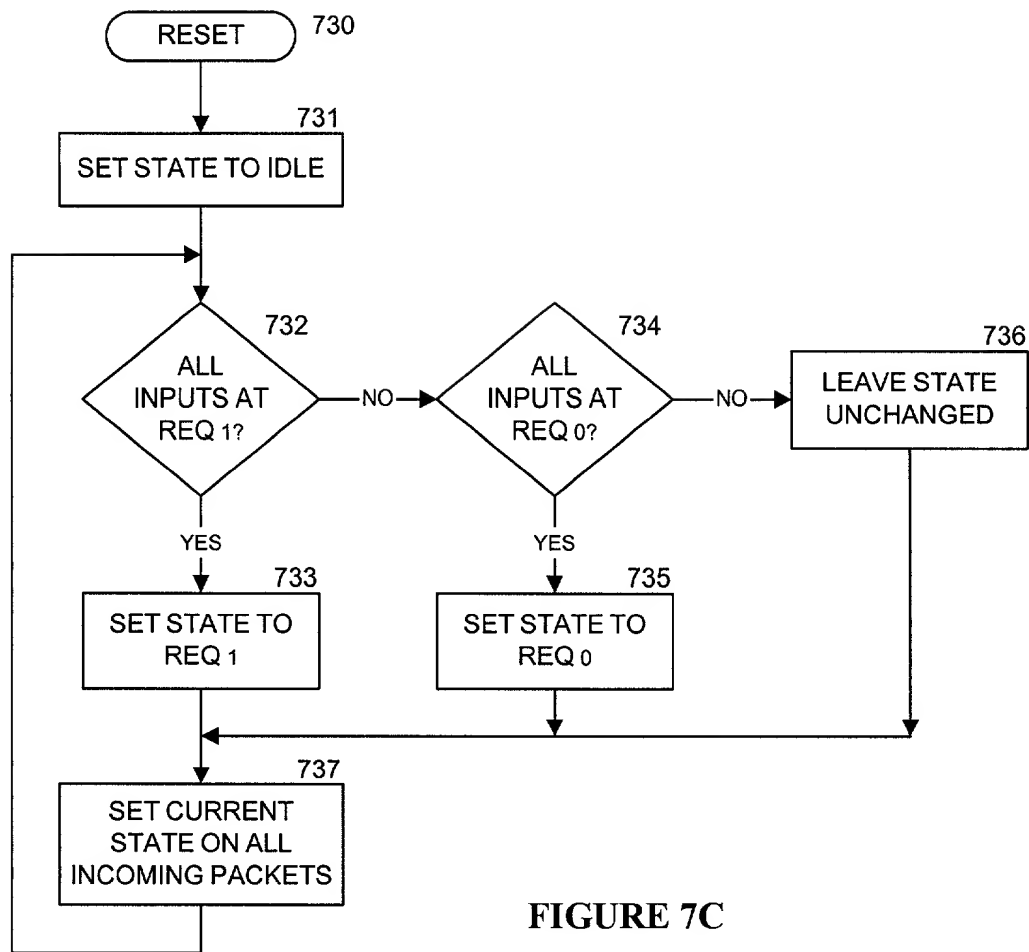
**FIGURE 7A**

```

graph TD
    706([RESET]) --> 707[SET STATE TO IDLE]
    707 --> 708{GOT ANY RX ACK?}
    708 -- NO --> 709[DELAY N PACKET TIMES]
    709 --> 710{GOT ANY RX ACK?}
    710 -- NO --> 711[SET STATE TO REQ 1]
    710 -- YES --> 712{ALL DEST'NS RX ACK 0?}
    708 -- YES --> 712
    712 -- YES --> 714[SET STATE TO REQ 1]
    712 -- NO --> 716{ALL DEST'NS RX ACK 1?}
    716 -- YES --> 718[SET STATE TO REQ 0]
    716 -- NO --> 720[LEAVE STATE UNCHANGED]
    714 --> 722[SET CURRENT STATE ON ALL OUTGOING PACKETS]
    718 --> 722
    720 --> 722
    722 --> 712

```

**FIGURE 7B**



**FIGURE 7C**

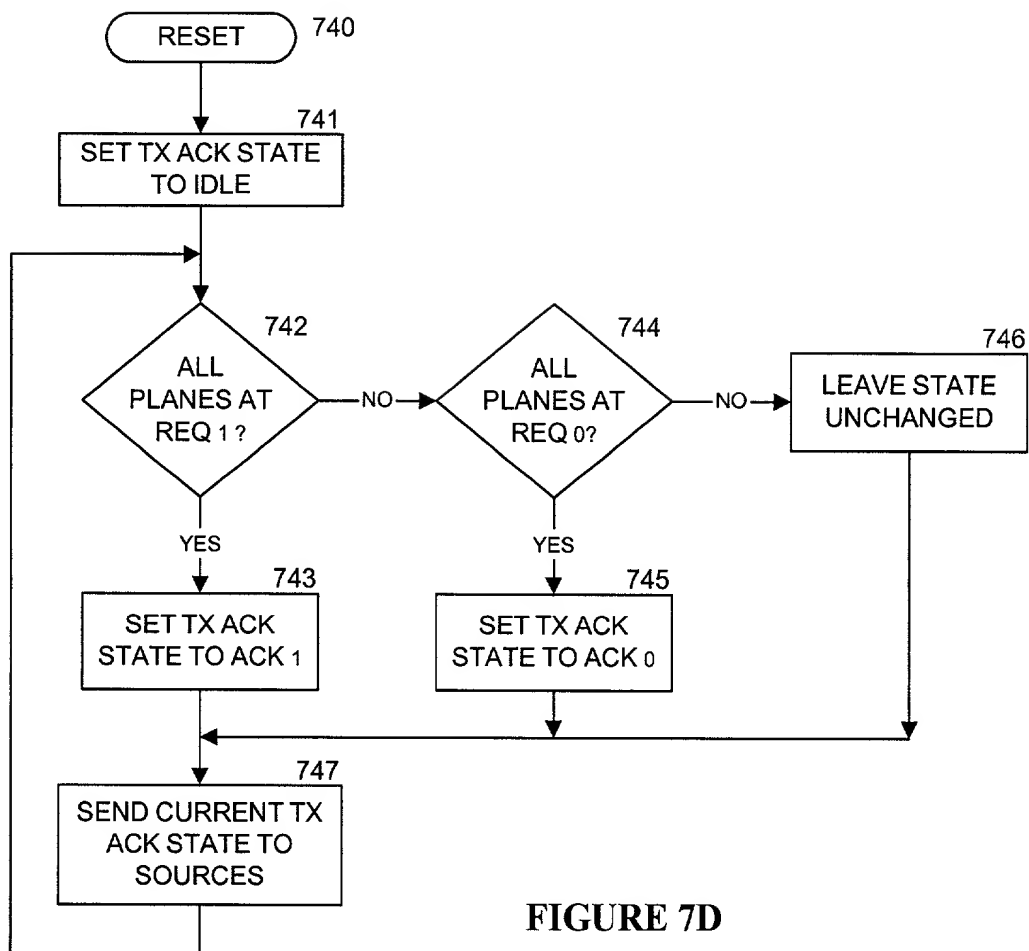


FIGURE 7D

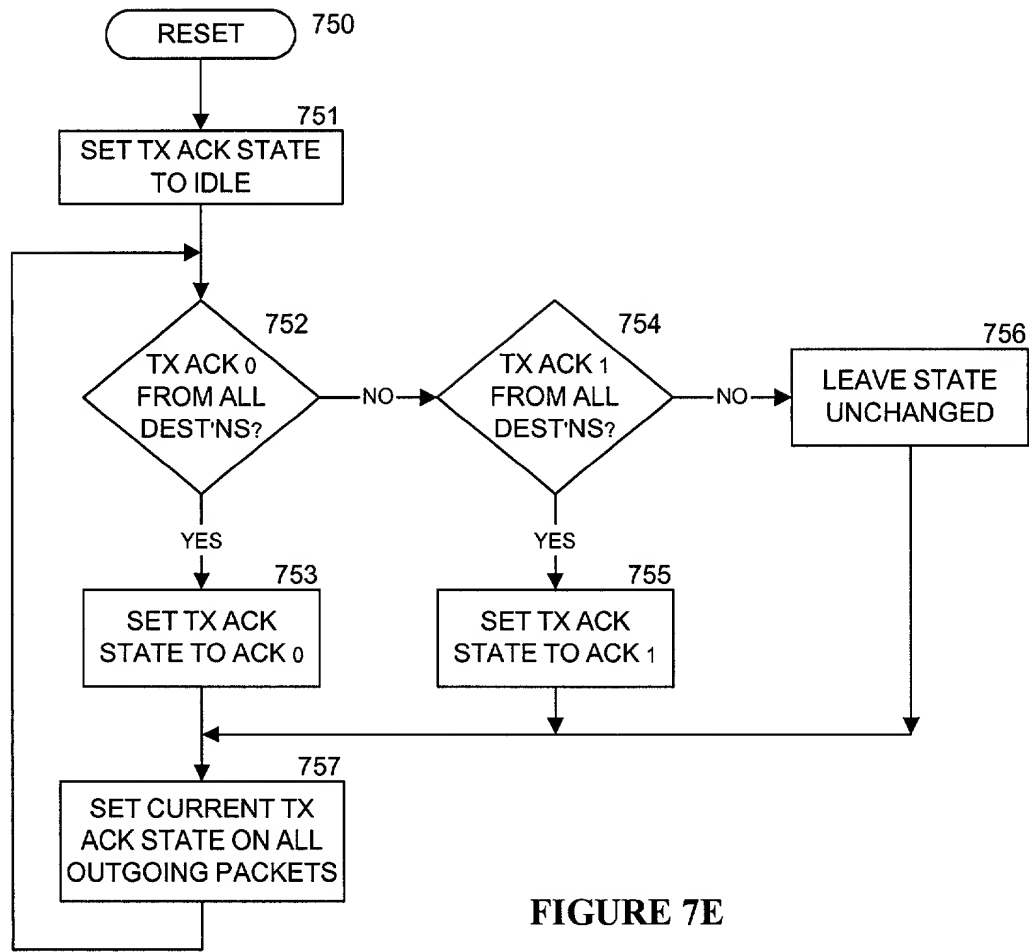


FIGURE 7E

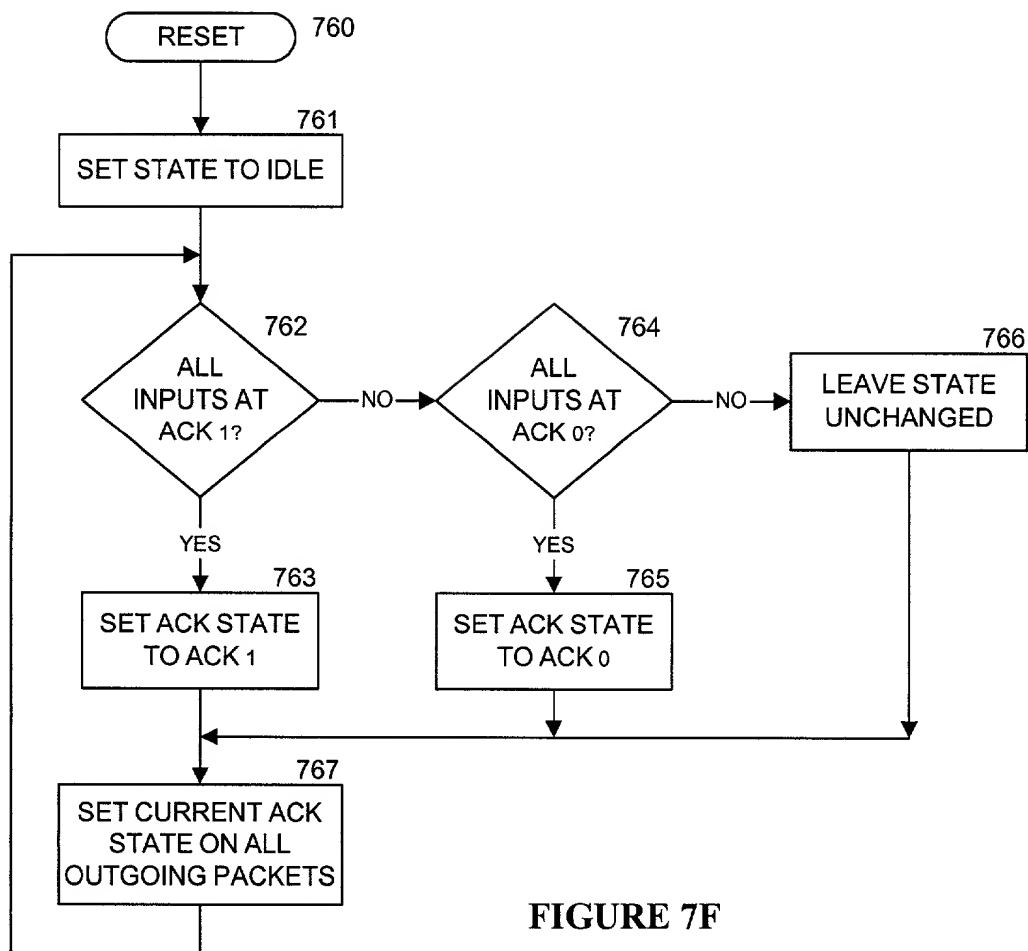


FIGURE 7F



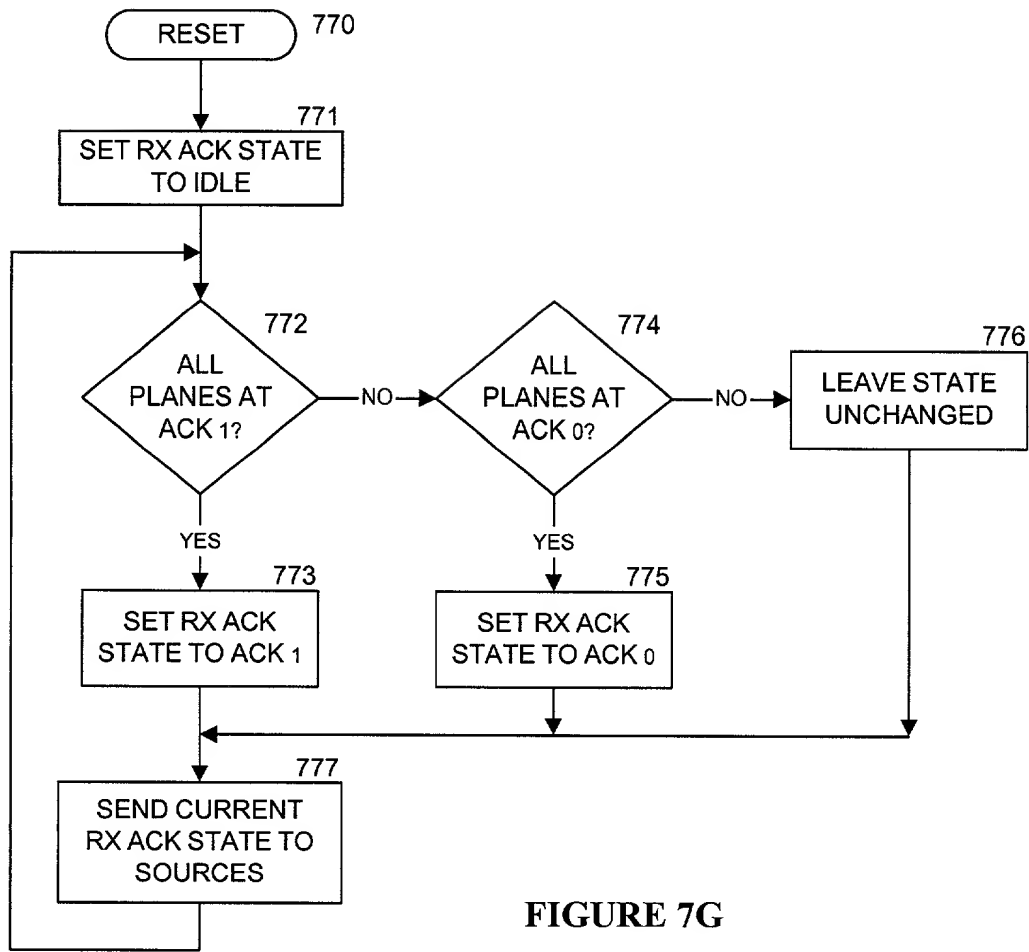


FIGURE 7G

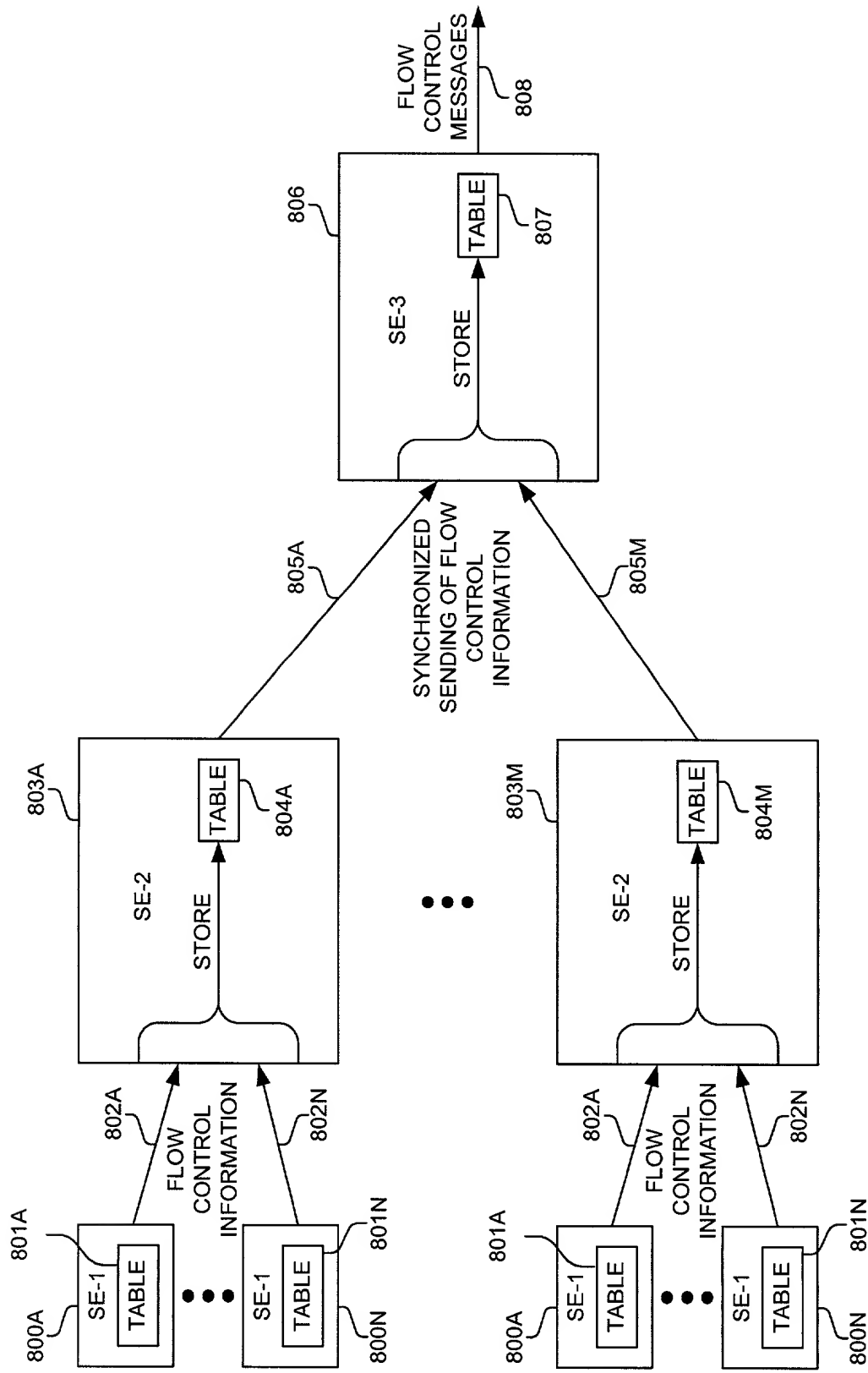
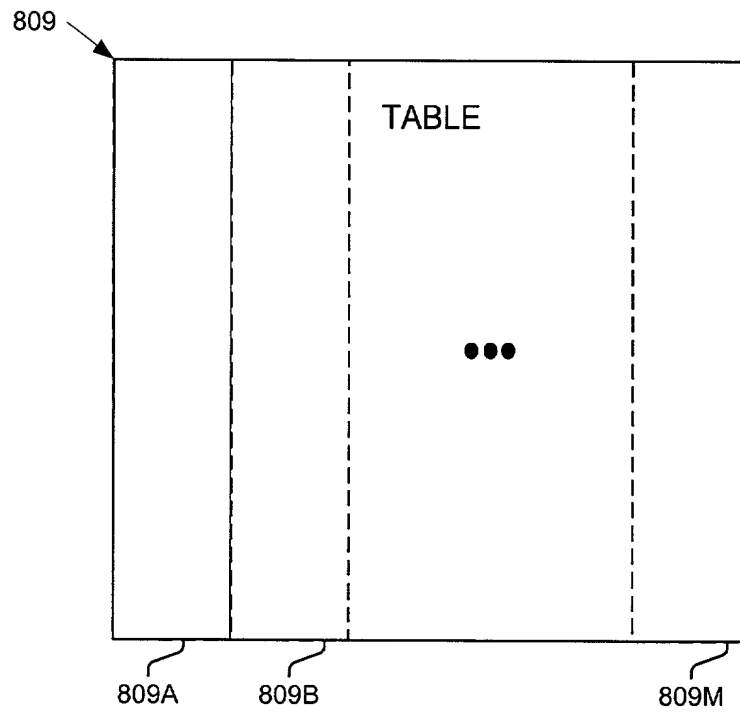
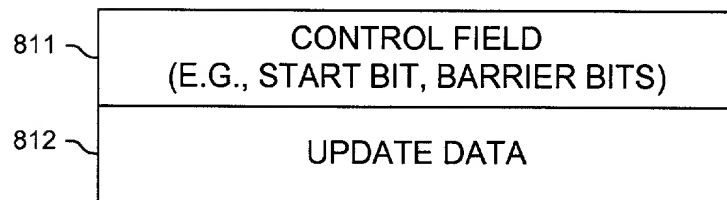


FIG. 8A



**FIGURE 8B**



**FIGURE 8C**

FIG. 8D

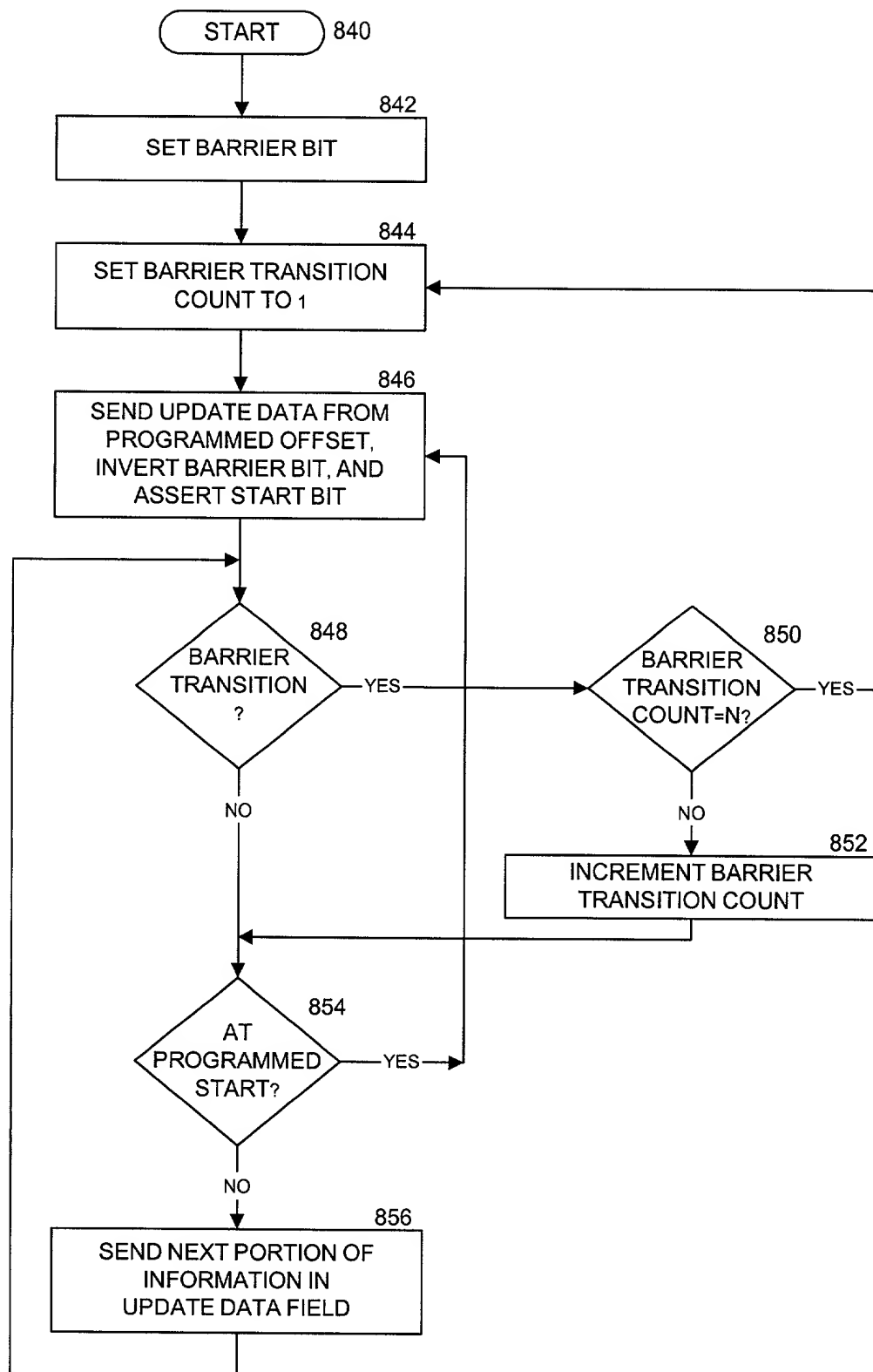


FIGURE 8D

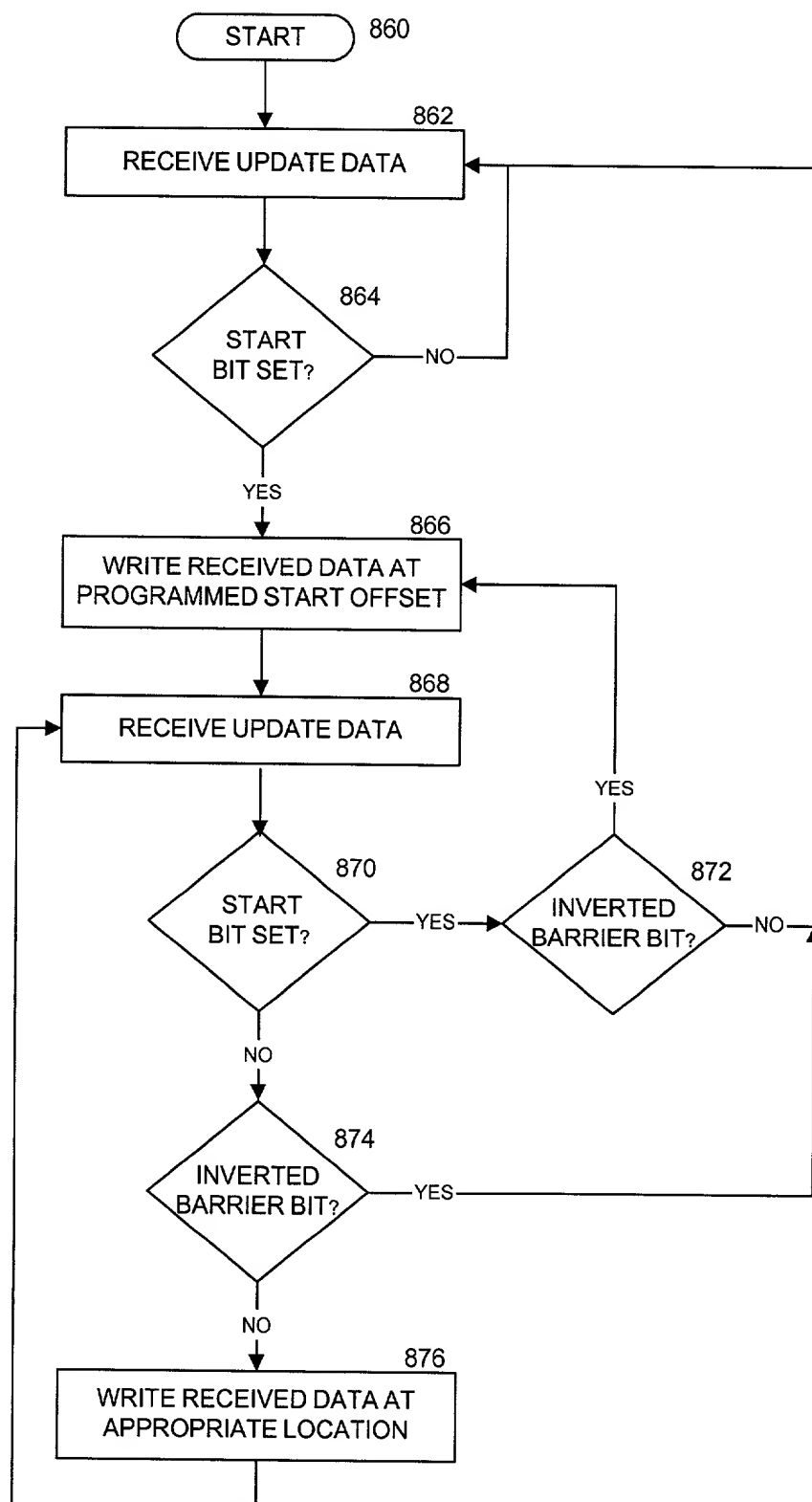


FIGURE 8E

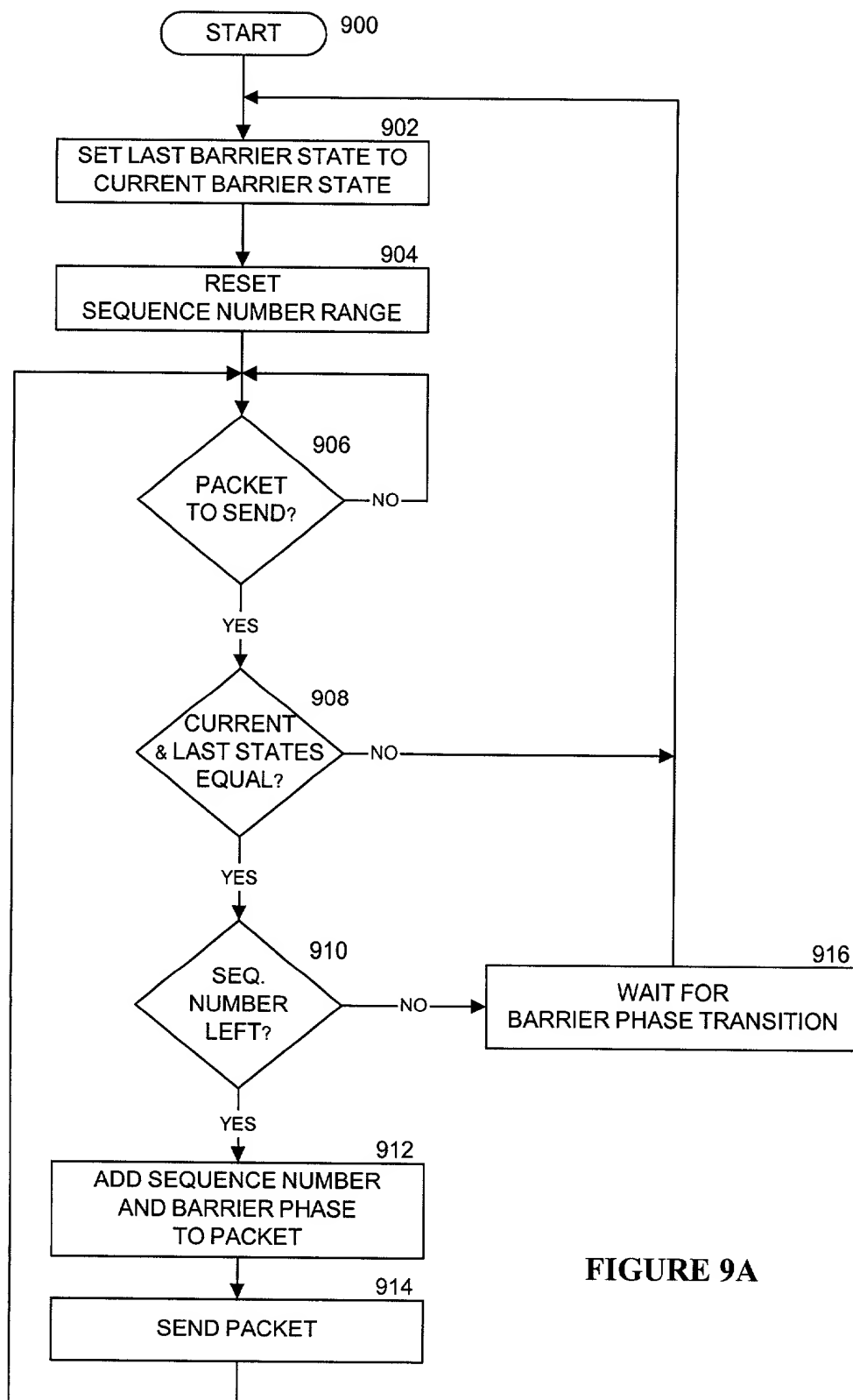


FIGURE 9A

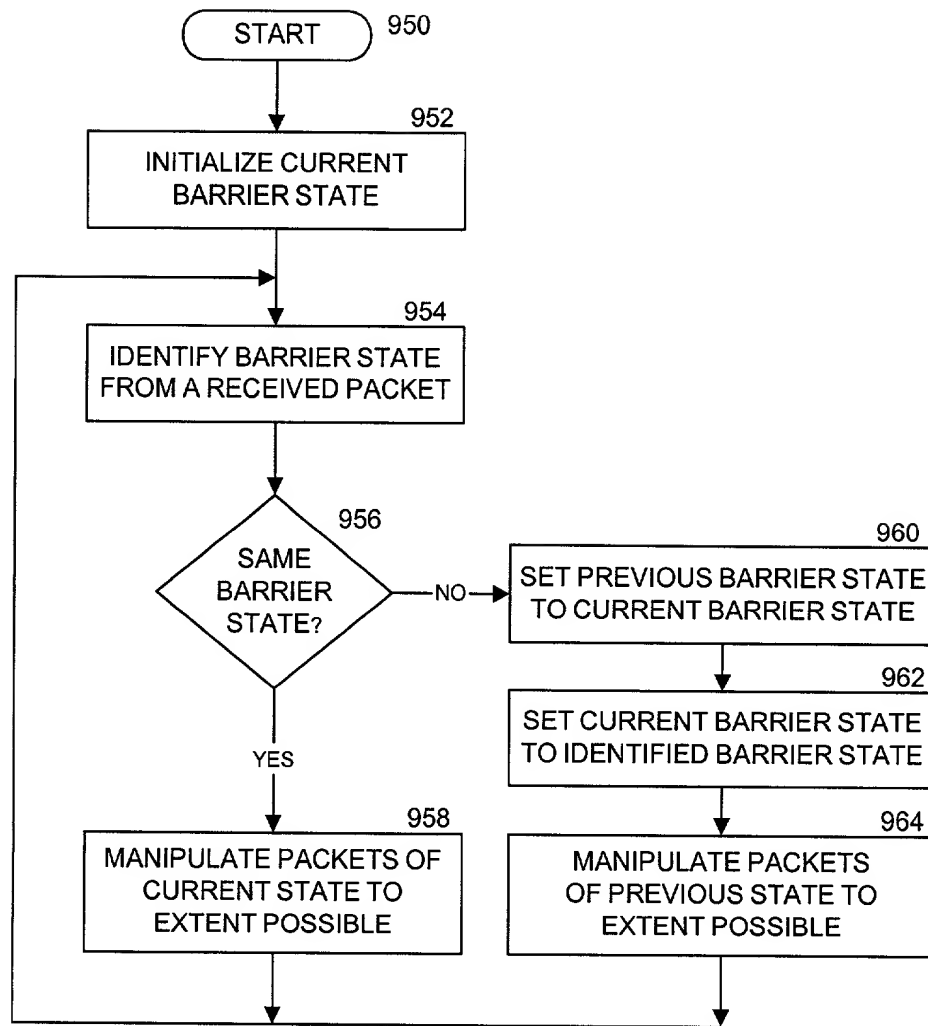


FIGURE 9B